

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10083402	02/27/2002	134 438	036	174 2812	EL ARINI

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\*\*CONTINUING DATA VERIFIED:  
None ZE

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\*\* FOREIGN APPLICATIONS VERIFIED:  
JAPAN 2001-259111 08/29/2001  
ZE

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		XA-9629
Verified and Acknowledged Examiners's initials ZE		
TITLE : Manufacturing method of semiconductor integrated circuit device		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
Amount Due		Sheets Drwg.	Fig. Drwg.
Date Paid		Print Fig.	
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
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